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APPLICANT: Fitzgerald et al.

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SERIAL NO: 09/923,207

EXAMINER: UNKNOWN

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FILED: 08/06/2001

FOR: GATE TECHNOLOGY FOR STRAINED SURFACE CHANNEL AND  
STRAINED BURIED CHANNEL MOSFET DEVICES

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

**INFORMATION DISCLOSURE STATEMENT**

In compliance with 37 C.F.R. §§1.56, 1.97, and 1.98, Applicant submits copies of the documents listed on the attached Form PTO-1449.

The listed documents were recently cited in a corresponding PCT application, and a copy of the International Search Report is being submitted herewith for purposes of convenience.

The Commissioner is authorized to charge Deposit Order Account No. 19-0079 for any further fee that is required.

Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Emily C. Porell

03/25/2002  
Date